

MICROPROGRAM MEMORY

RAM & MUX	7				6				5	4				3			2				1				0				NOTES										
	BRANCH ADDRESS				NEXT INSTRUCTION CONTROL				DEST SELECT	SOURCE SELECT				ALU			'A'				'B'				'D'				NEXT ADDRESS CONTROL	DATA CONTROL									
MEMORY ADDRESS	BR <sub>3</sub>	BR <sub>2</sub>	BR <sub>1</sub>	BR <sub>0</sub>	P <sub>3</sub>	P <sub>2</sub>	P <sub>1</sub>	P <sub>0</sub>	MUX <sub>1</sub>	I <sub>8</sub>	I <sub>7</sub>	I <sub>6</sub>	MUX <sub>0</sub>	I <sub>2</sub>	I <sub>1</sub>	I <sub>0</sub>	C <sub>n</sub>	I <sub>5</sub>	I <sub>4</sub>	I <sub>3</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	B <sub>3</sub>	B <sub>2</sub>	B <sub>1</sub>	B <sub>0</sub>	D <sub>3</sub>	D <sub>2</sub>	D <sub>1</sub>	D <sub>0</sub>							
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BLANK = DON'T CARE

INSTRUCTION \_\_\_\_\_

